

09/703,831

04870009AA

Reply to office action mailed 09/23/2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re patent application of

Yamada et al.

Serial No. 09/702,831

Group Art Unit 2829

Filed: November 11, 2000

Examiner Nguyen, Vinh

For SEMICONDUCTOR DEVICE TESTER

Box Non-Fee Amendment
Commissioner for Patents
Alexandria, VA 22313 - 1450

PROPOSED CHANGES TO THE DRAWINGS

Sir:

Attached hereto is a table showing several proposed changes to the drawing figures. In the concurrently filed amendment, it is demonstrated that the revisions to the drawing figures are supported by the specification and claims as originally filed. An indication that the drawing figures are satisfactory is requested with the next office action.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Michael E. Whitham".

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Reg. No. 32,635

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Please associate this
Application with
Customer No. 30743

703-787-9400
703-787-7557 (fax)

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09/703,831

04870009AA

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Amendments to the Drawings:

The attached drawing sheets are proposed to replace the original drawing sheets as follows:

Sheet	Figures	Sheet	Figures
4	4a	16	17a
5	5a	17	18a
6	6a, 6b	18	19a
7	7a, 7b,	19	20a
9	10a, 10b	20	21a
10	11a, 11b	21	22b
11	12a	34	40
13	14a, 14b	42	49, 50
14	15a, 15b	43	51, 52
15	16a	44	53, 54

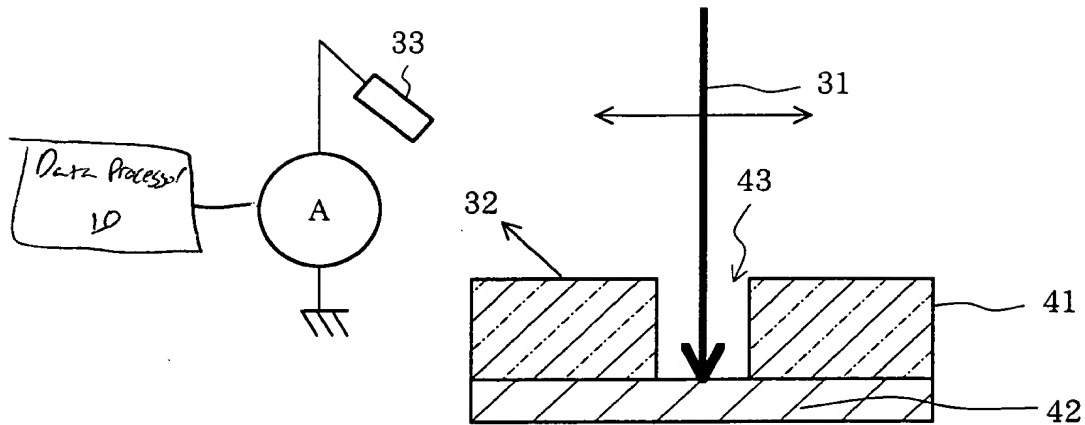


FIG.4a

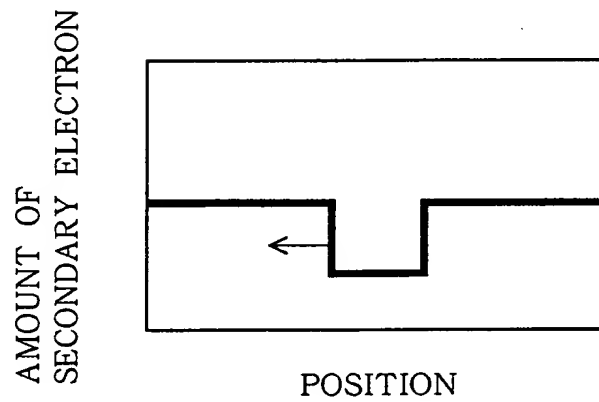


FIG.4b

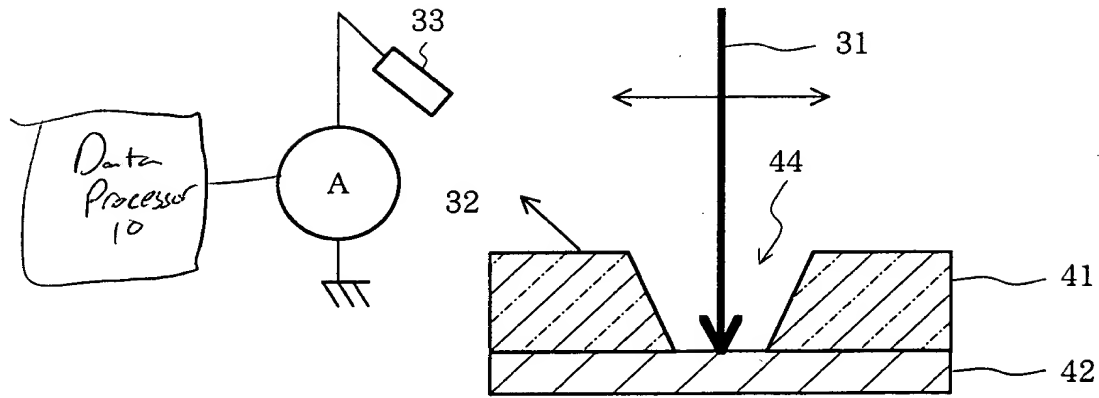
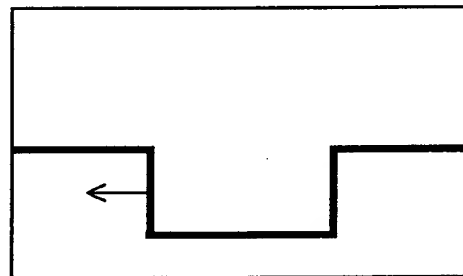


FIG.5a

AMOUNT OF
SECONDARY ELECTRON



POSITION

FIG.5b

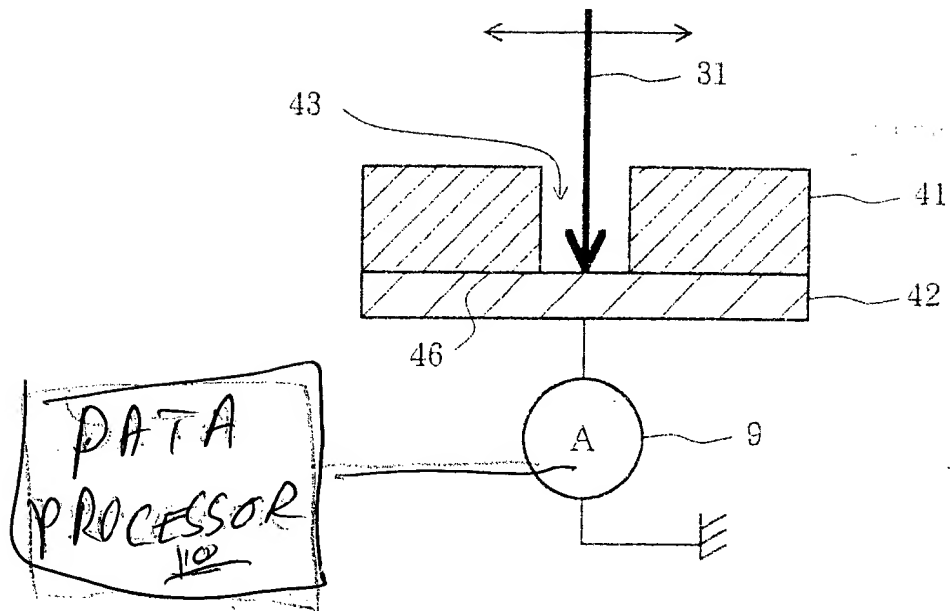


FIG.6a

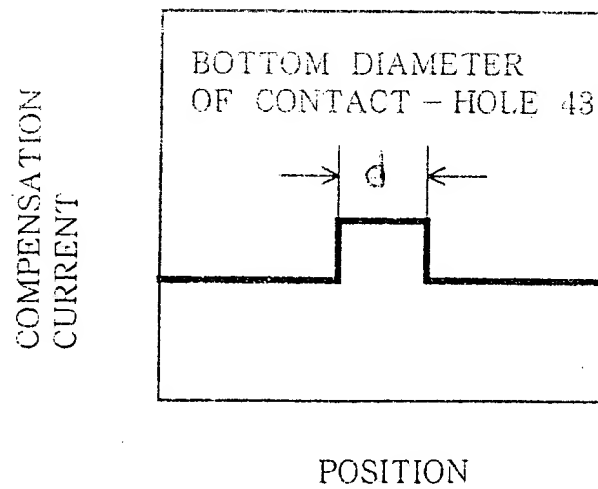


FIG.6b



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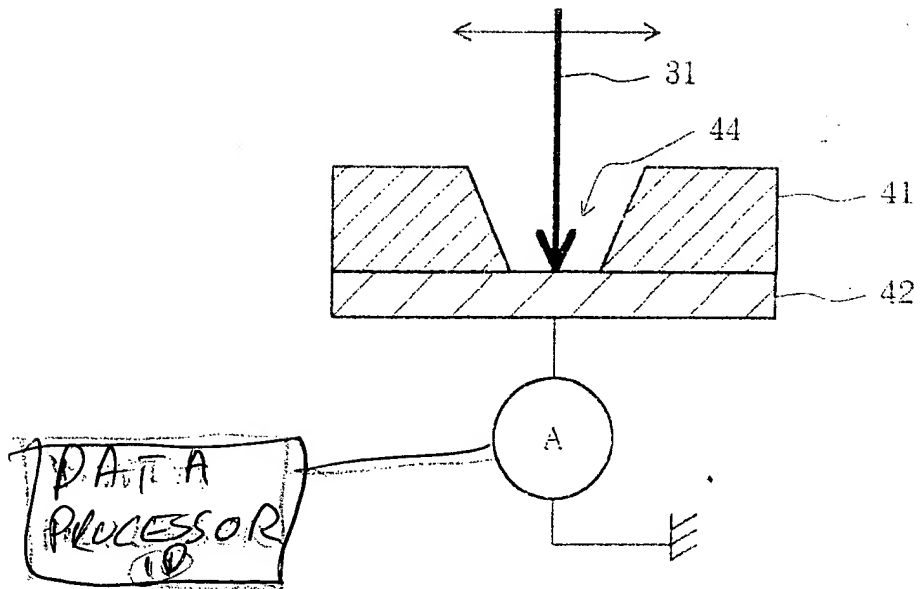
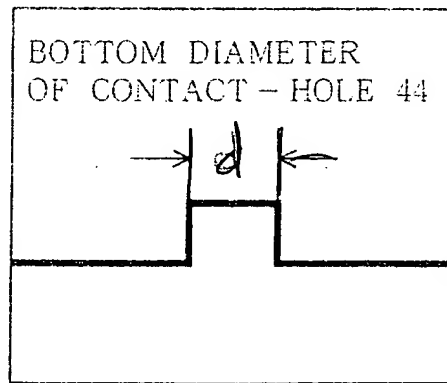


FIG.7a

COMPENSATION CURRENT



POSITION

FIG.7b

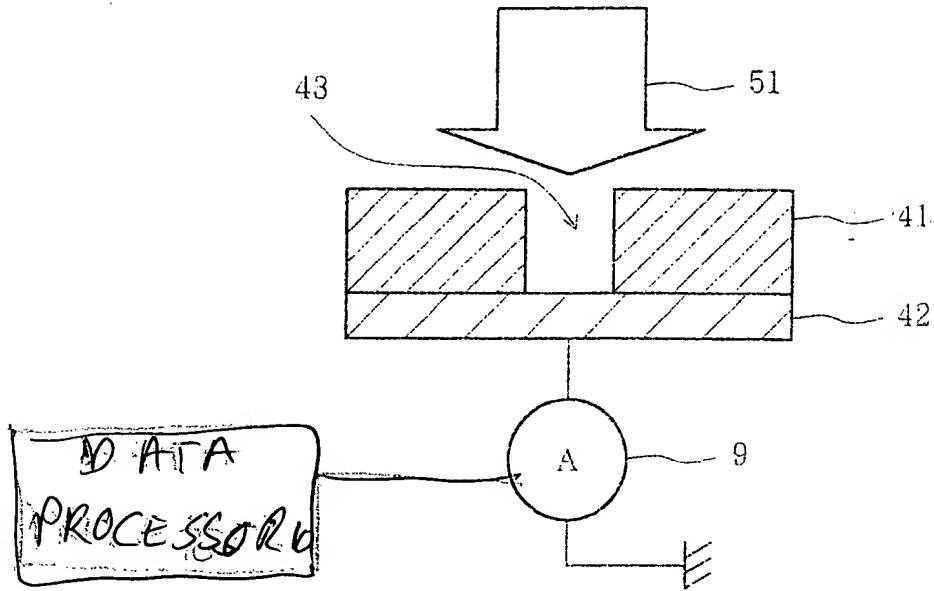
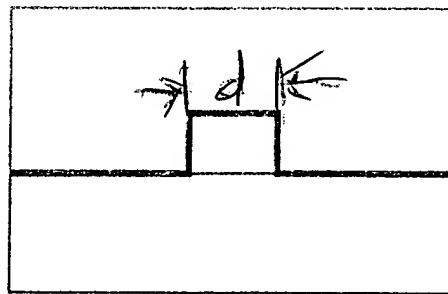


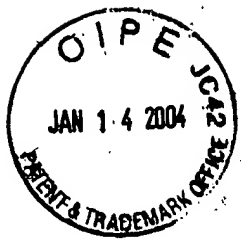
FIG. 10a

COMPENSATION CURRENT



POSITION

FIG. 10b



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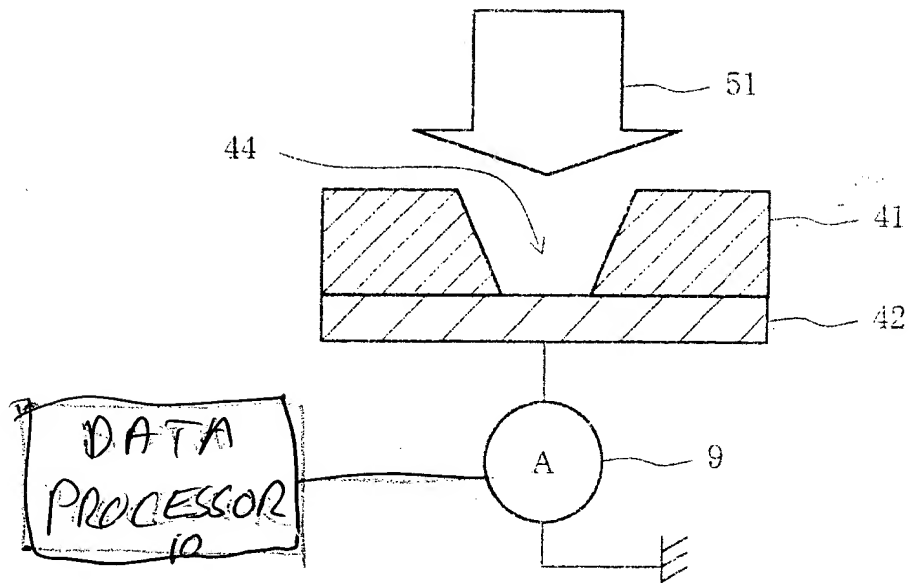
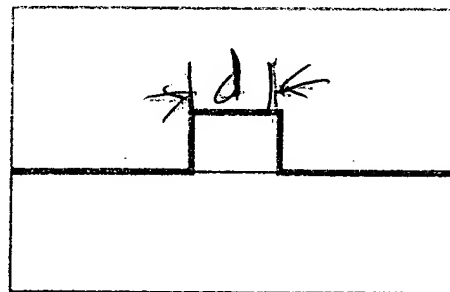


FIG.11a

COMPENSATION CURRENT



POSITION

FIG.11b

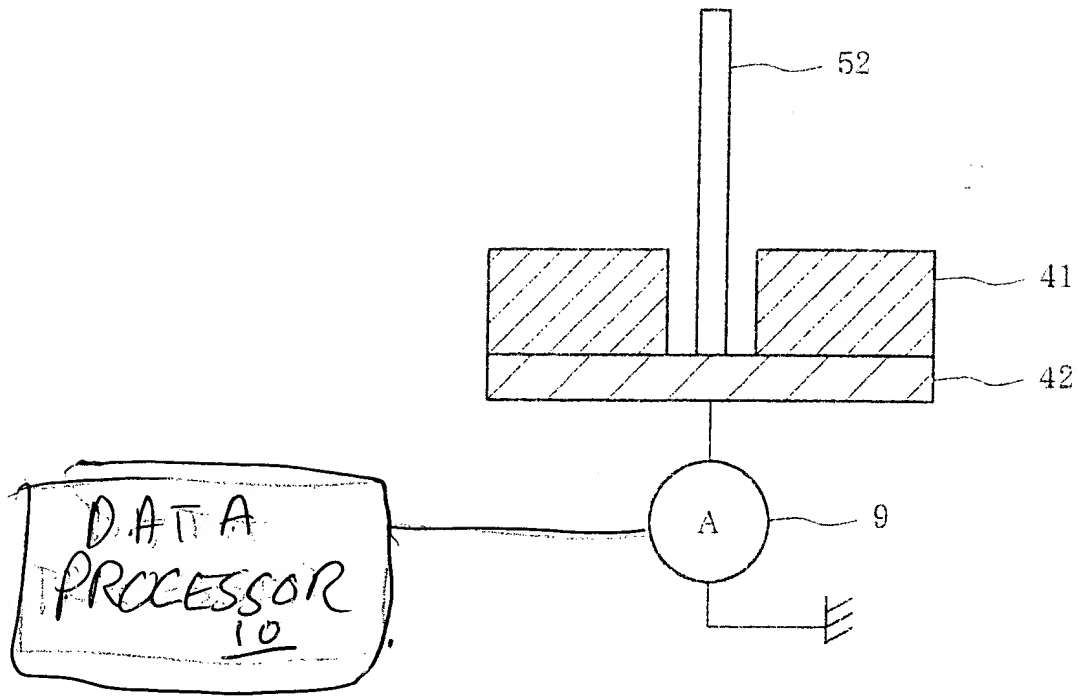
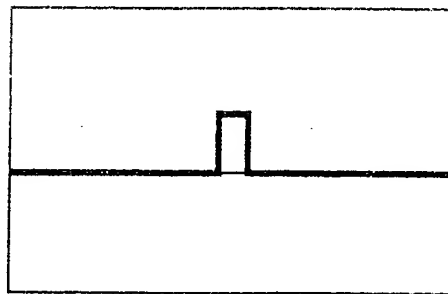


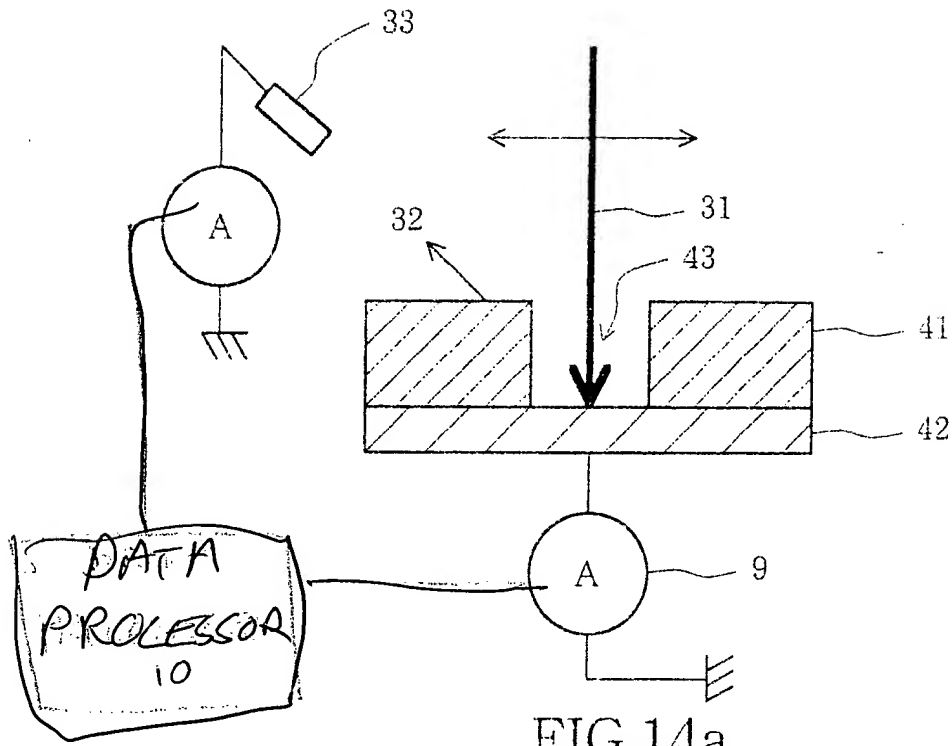
FIG.12a

COMPENSATION CURRENT

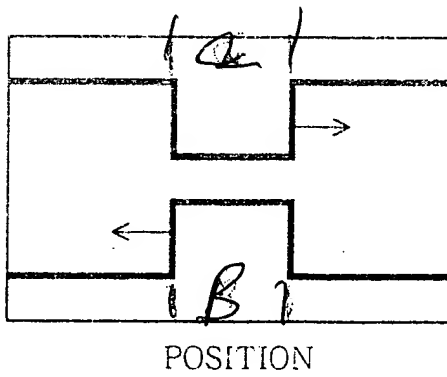


POSITION

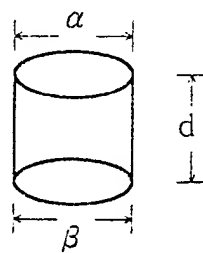
FIG.12b



COMPENSATION CURRENT



AMOUNT OF SECONDARY CURRENT



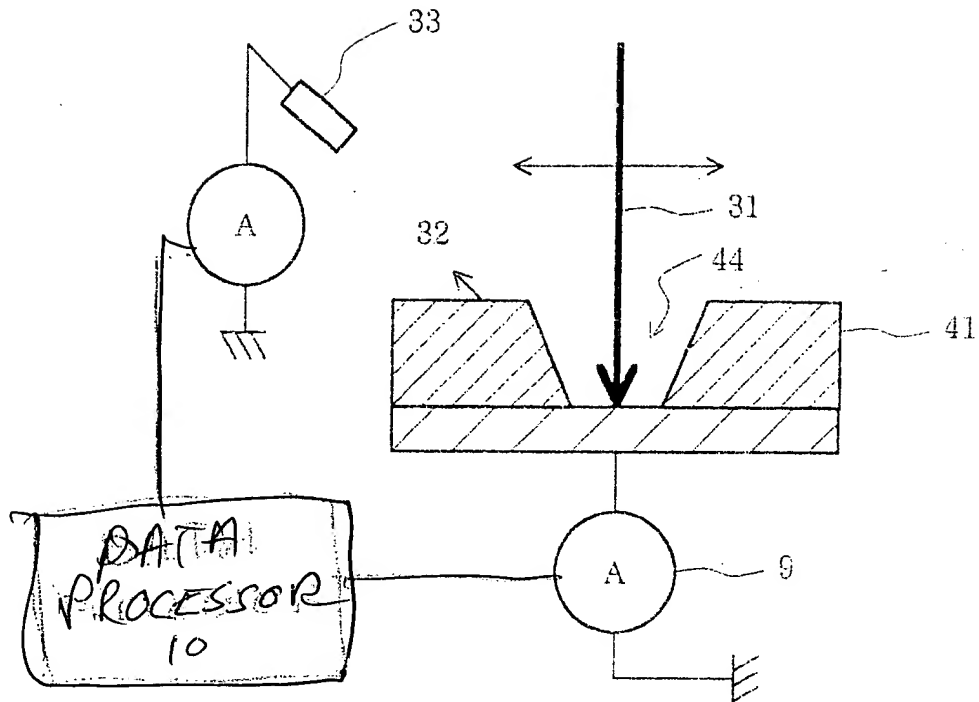


FIG. 15a

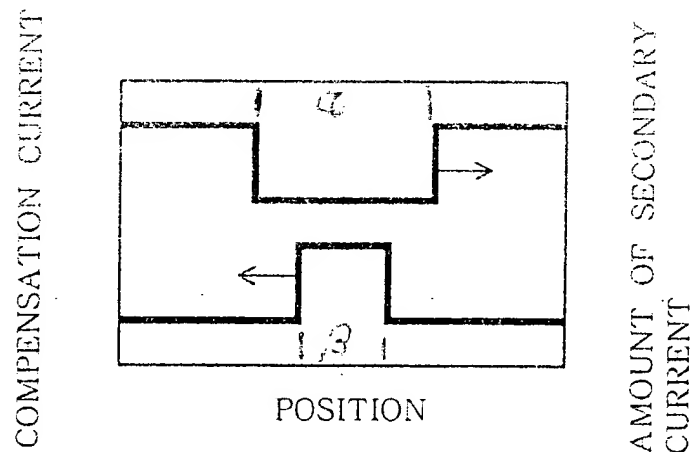


FIG. 15b

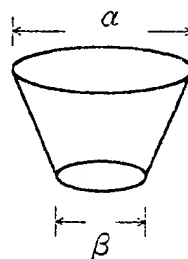
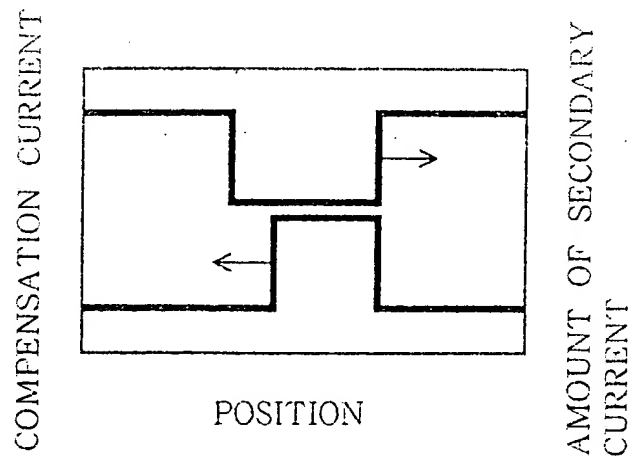
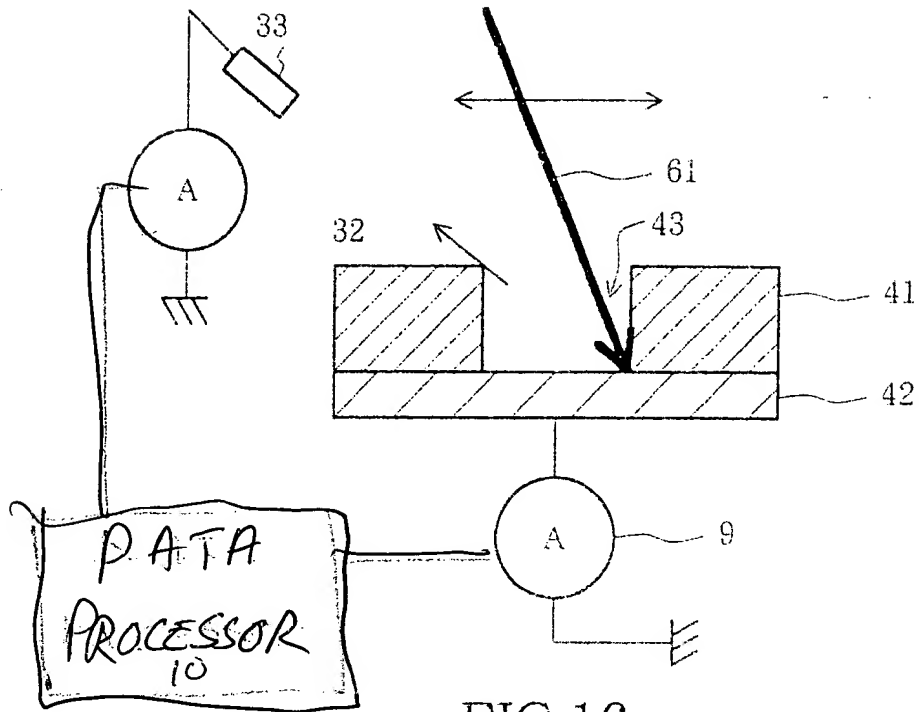


FIG. 15c



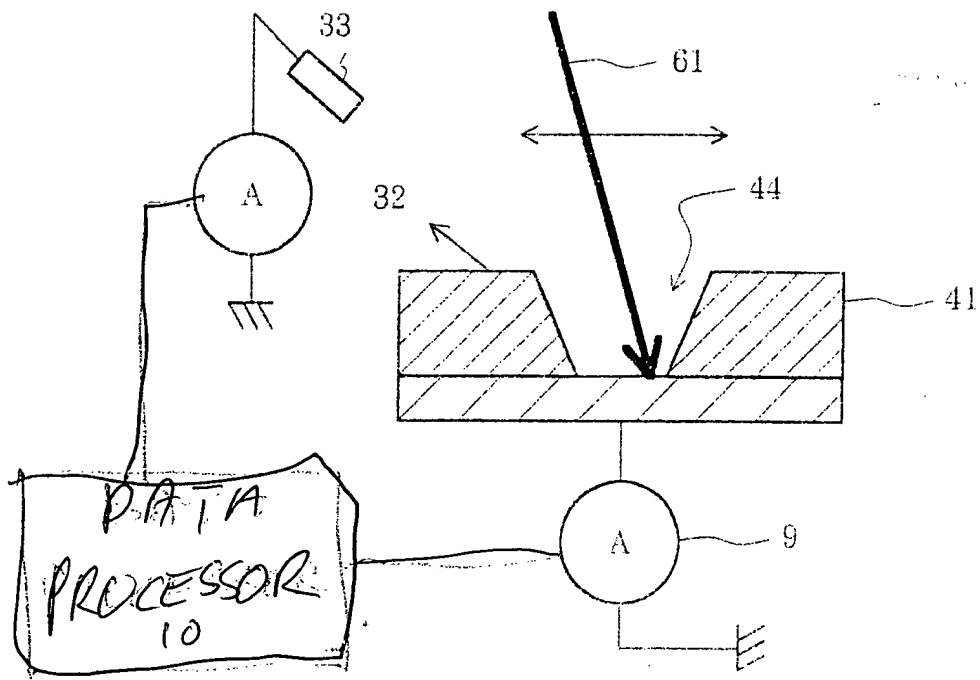


FIG.17a

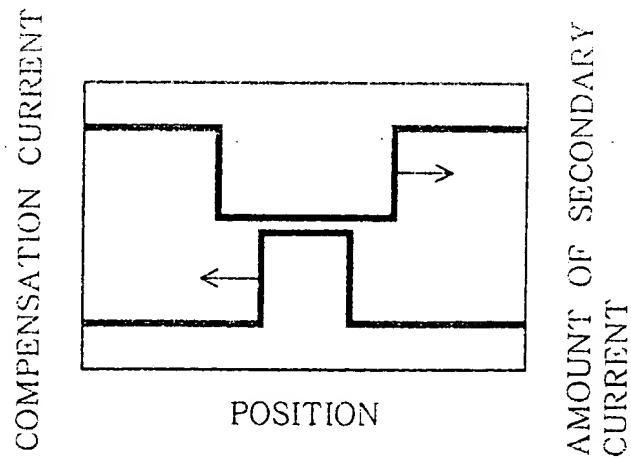


FIG.17b

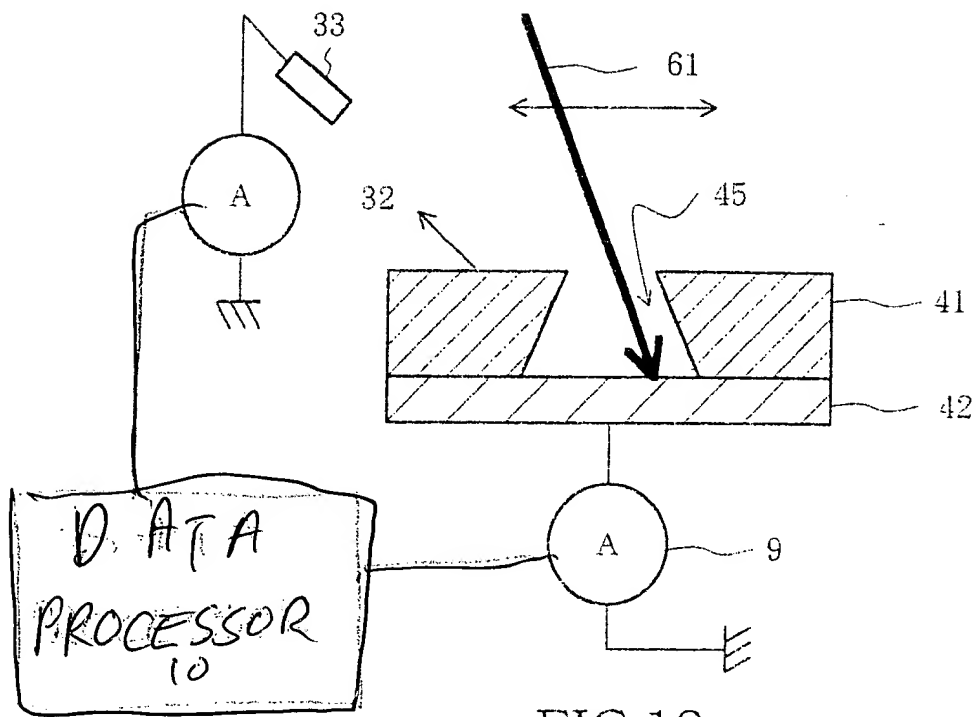


FIG. 18a

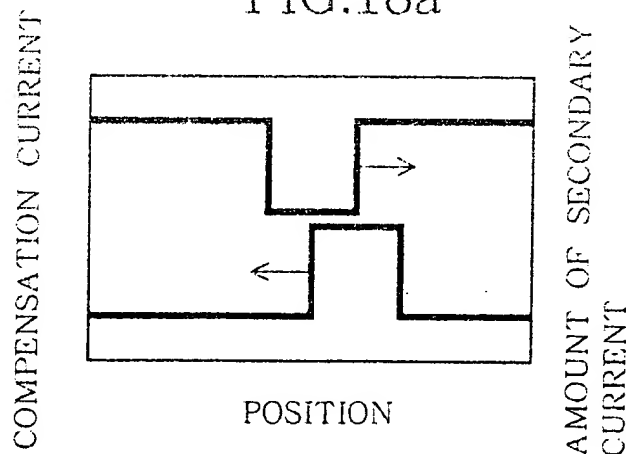


FIG. 18b



FIG. 18c



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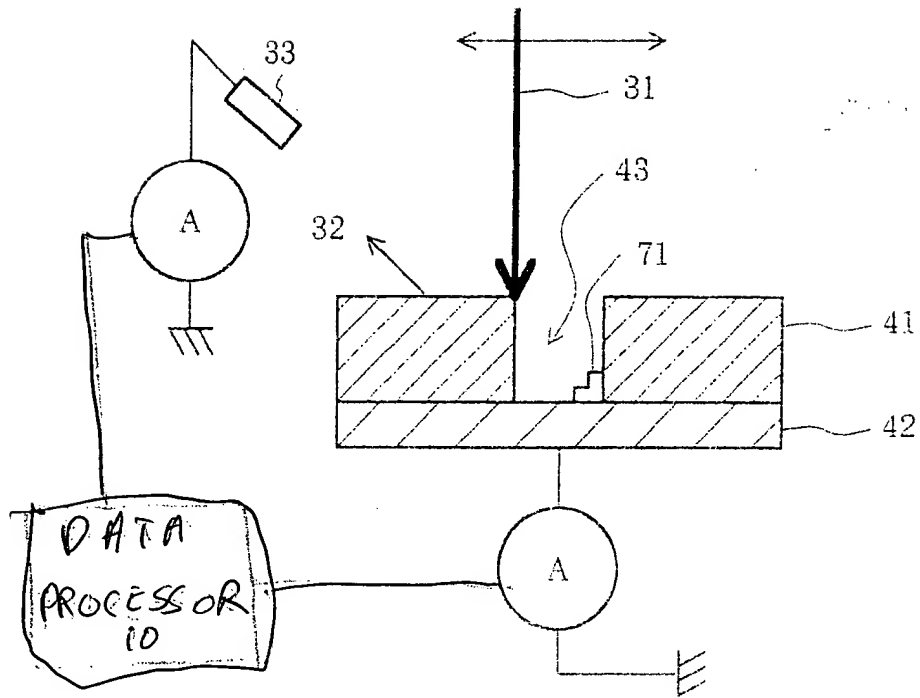


FIG.19a

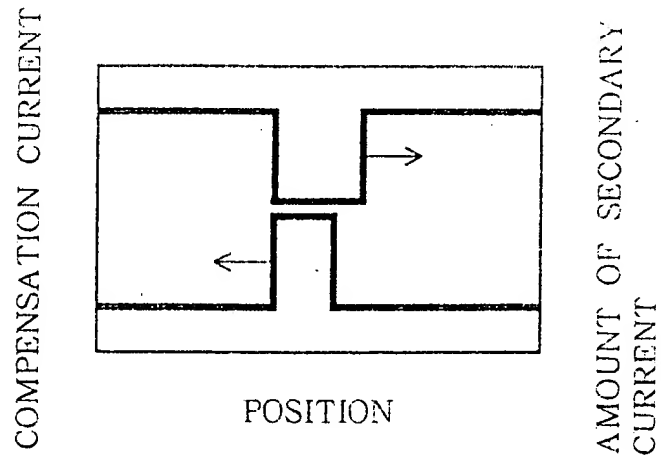


FIG.19b

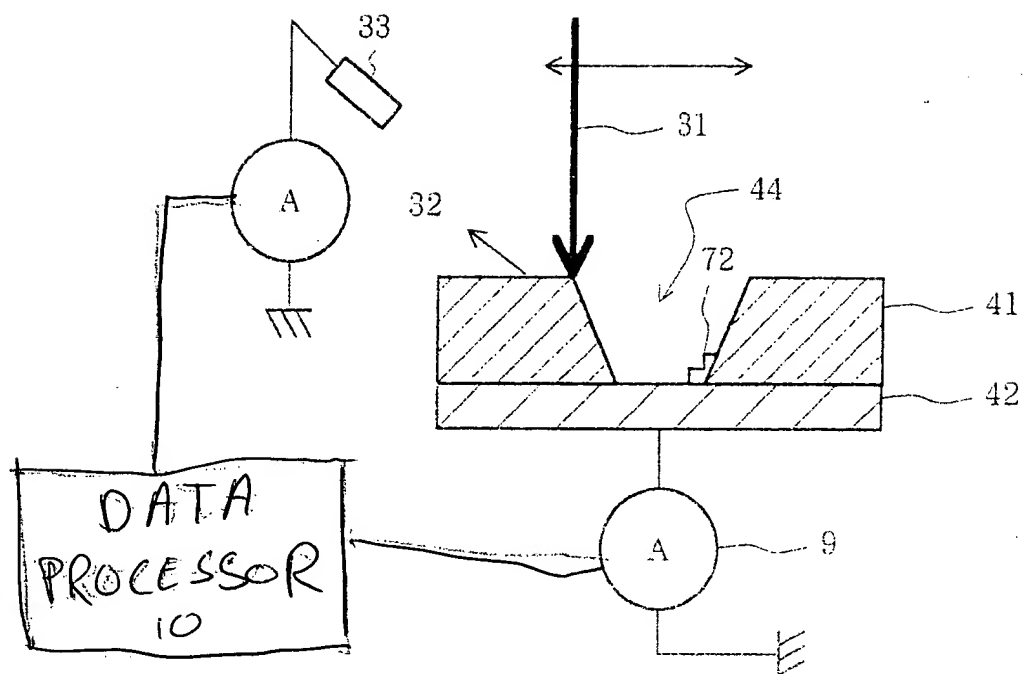


FIG.20a

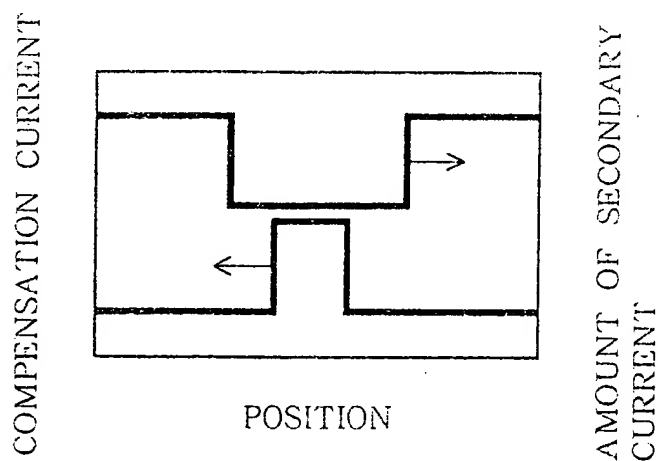


FIG.20b

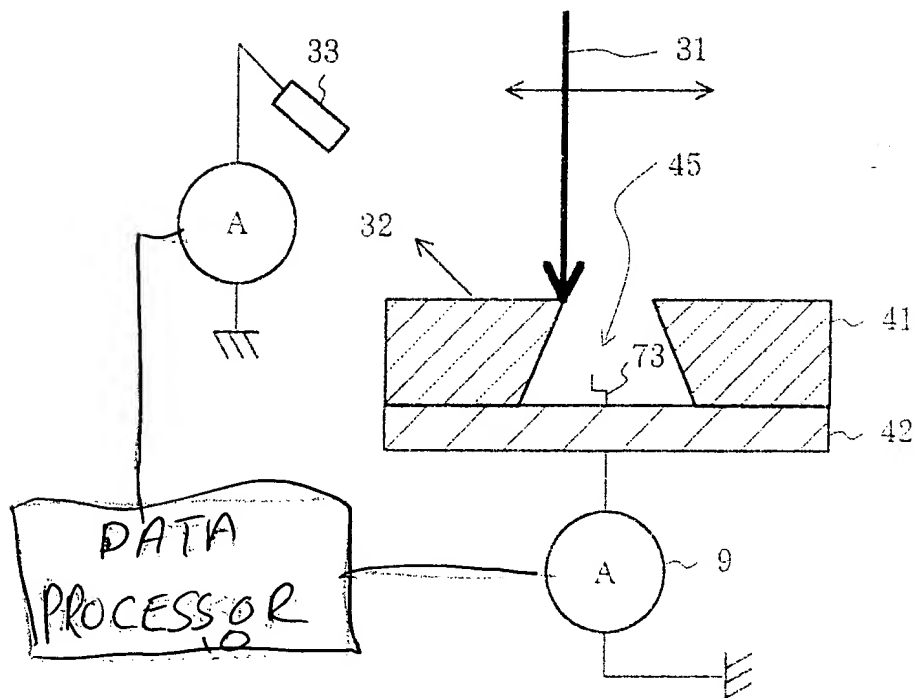


FIG.21a

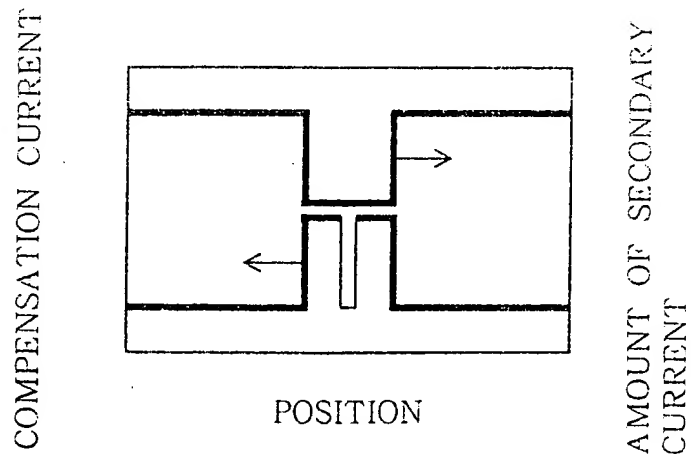


FIG.21b

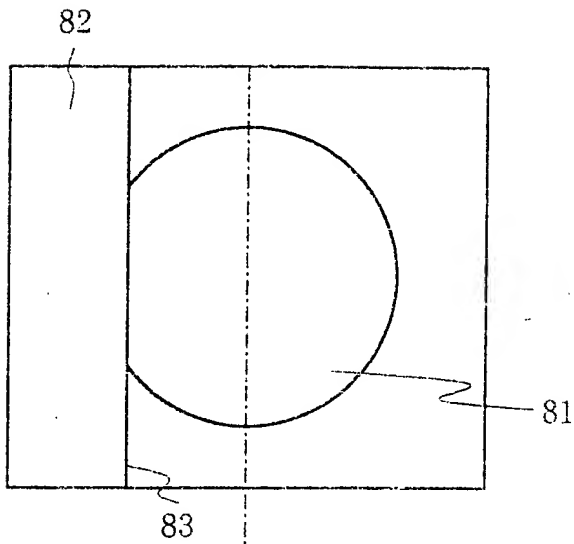


FIG. 22a

DATA
PROCESSOR
10

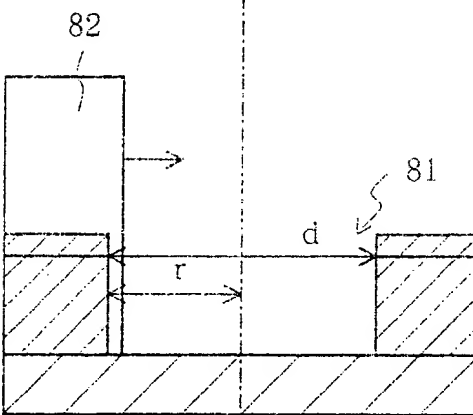
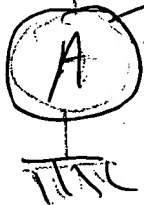


FIG. 22b

COMPENSATION CURRENT

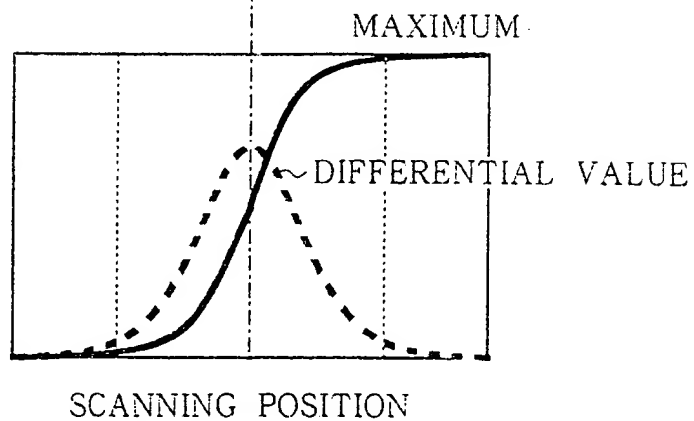
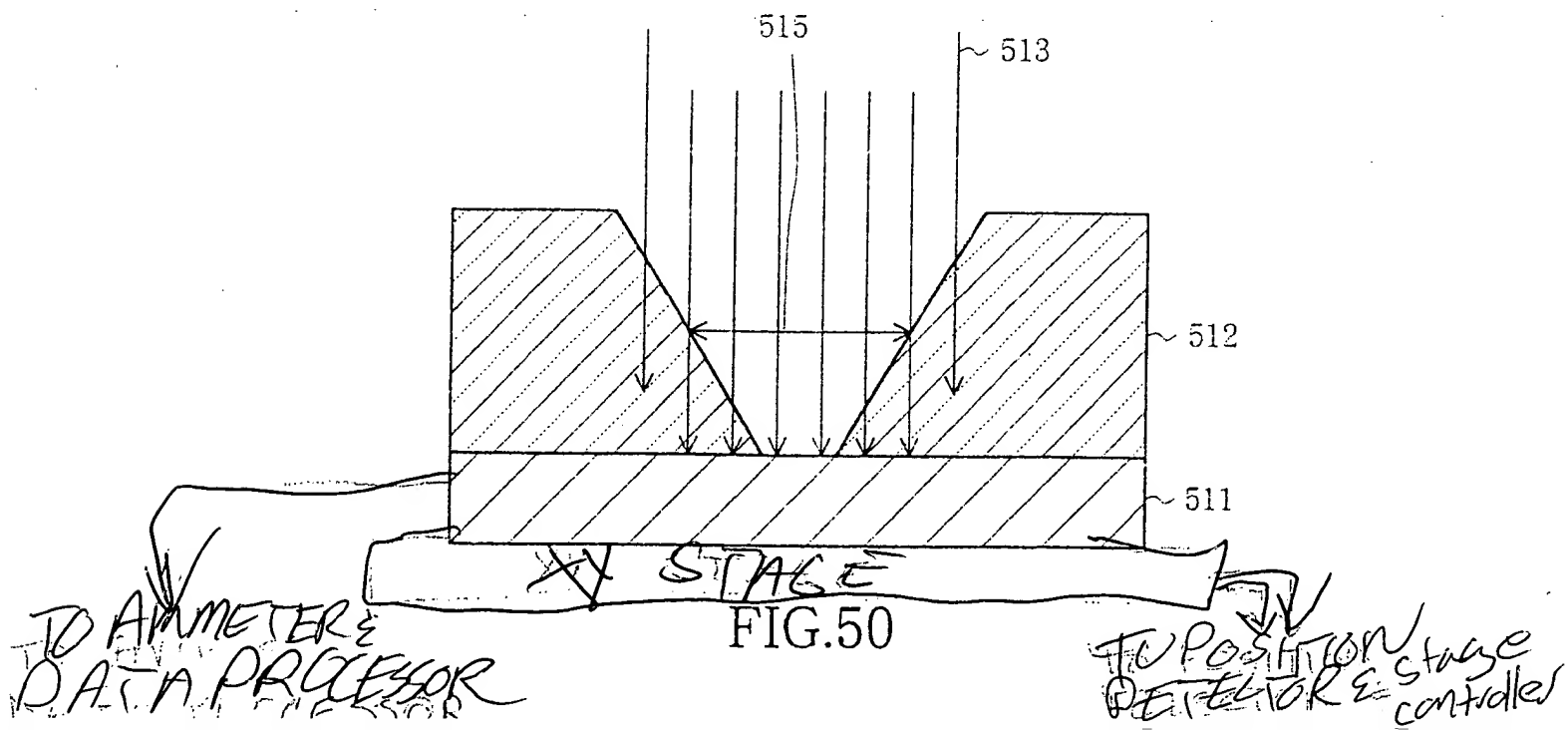
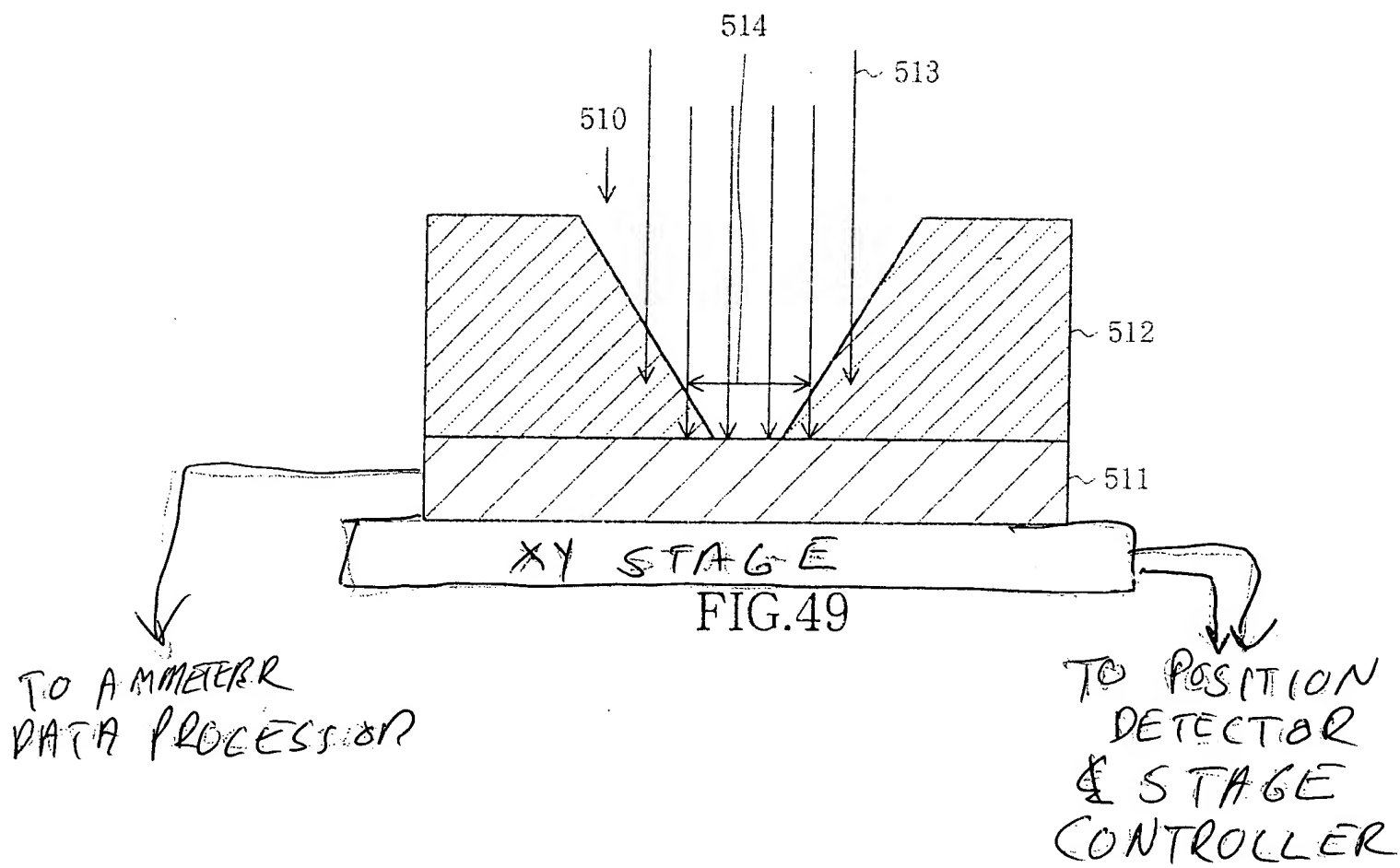
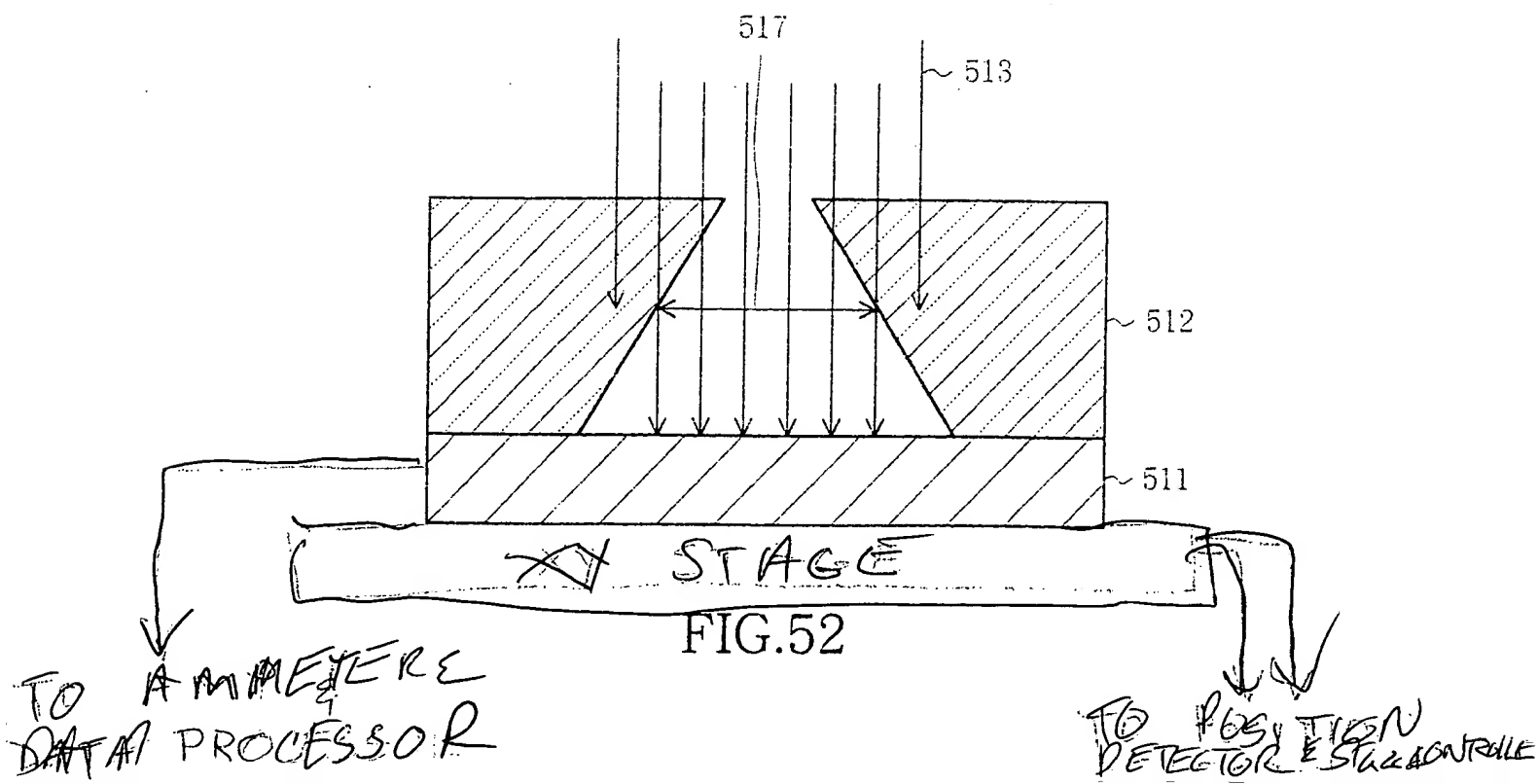
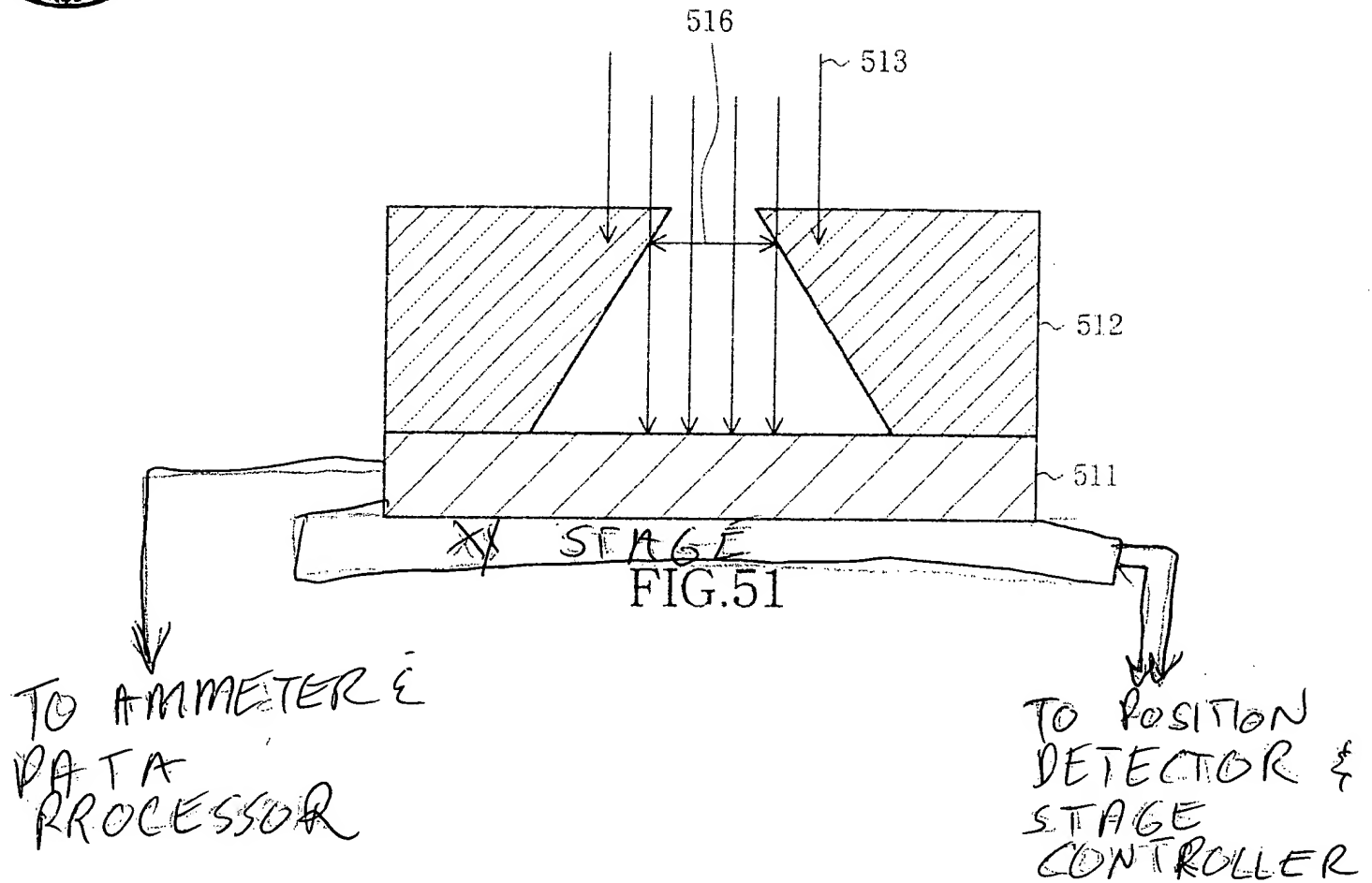


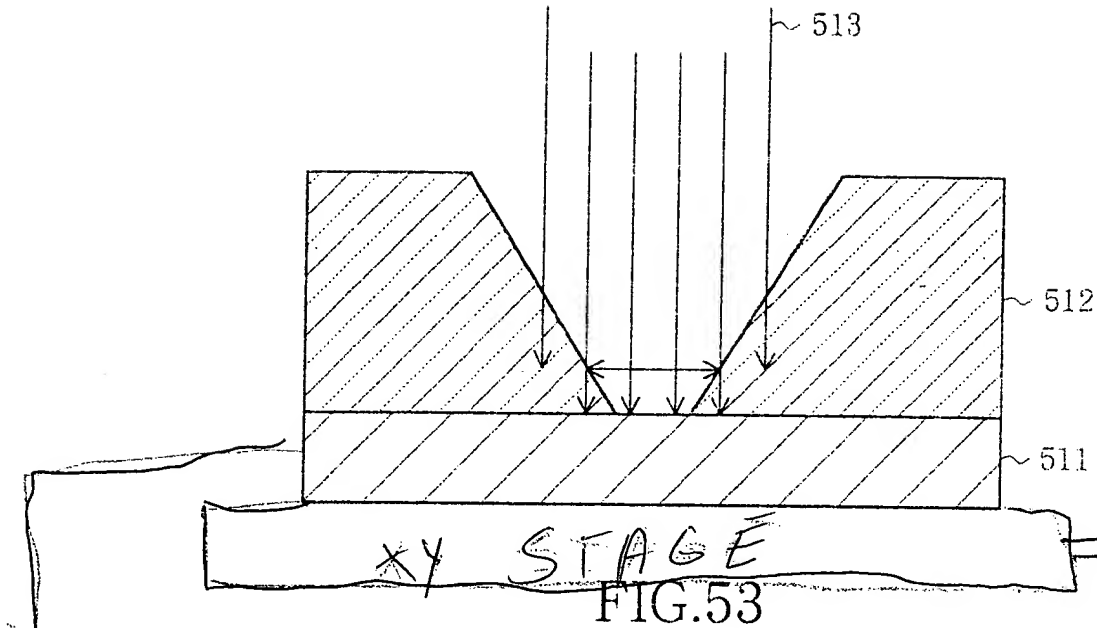
FIG. 22c



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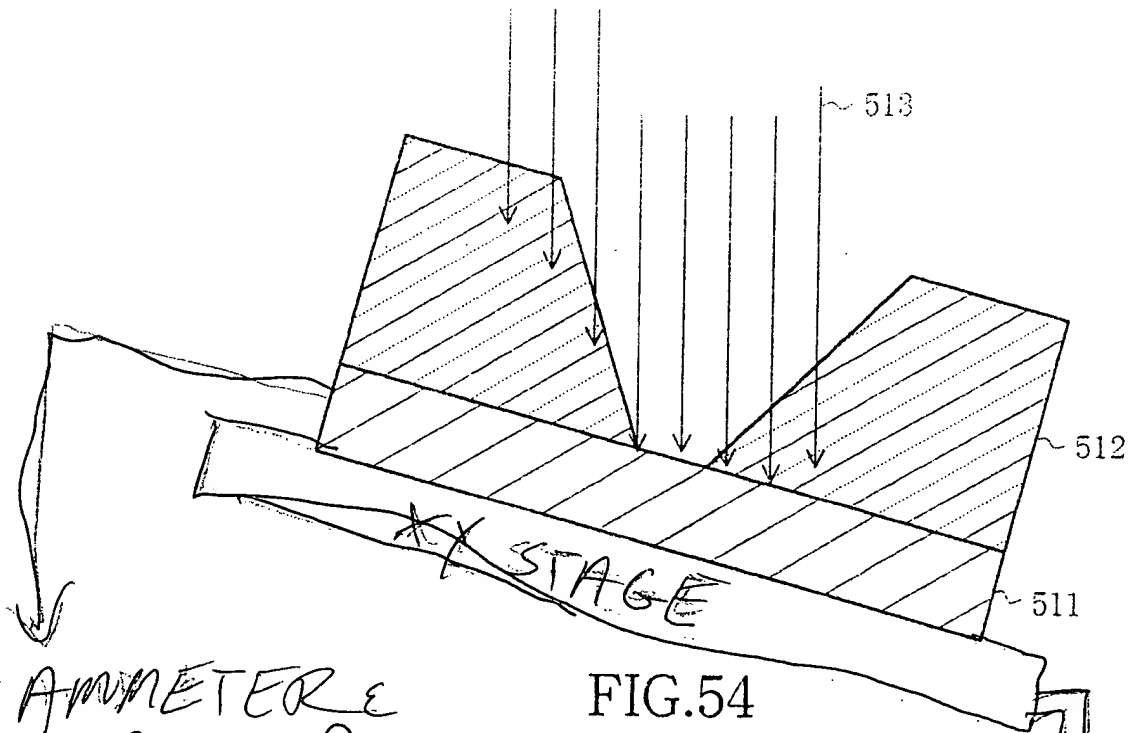






TO AMMETER &
DATA PROCESSOR

TO POSITION
DETECTOR &
STAGE CONTROLLER



TO AMMETER &
DATA PROCESSOR

TO POSITION
DETECTOR &
STAGE CONTROLLER